

# Electrical Engineering Lab

## Student Group

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## Table of Contents

- Electrical Engineering Lab** ..... 2
- Preparation*** ..... 2
- Experiments ..... 2
- Semester, group, and time allocation ..... 2
- Oral examination ..... 3
- Location of the labs** ..... 3
- Further links** ..... 4

# Electrical Engineering Lab

## Preparation



Before you are allowed to participate in the lab, you must have read the [lab regulations](#).

For insurance reasons, this must be confirmed at the first appointment before the experiments begin.

Please note that an attendance list will be provided at every on-site session.



- The tasks are worked on synchronously during the lab session. This means nobody can leave early.
- You must **print the assignment yourself**.

- Please read the experiment script and the “Preparation for the short test” thoroughly before each experiment.
- For carrying out the experiments, some [Hints for Electrical Setups](#) have been compiled to make your life easier.

## Experiments



Fig. 1: ET1 Lab in SS2020

- You can find the experiment scripts in [ILIAS](#).
- There are 6 on-site experiments at the university.
- The preparation for the short test can be found here in the wiki under each experiment (see the menu bar on the left).

## Semester, group, and time allocation

- The semester allocation is shown below.
- The [group allocation](#) will be available in ILIAS from one week before lectures start.
- The course is worth 2 ECTS. This corresponds to about 50...60 hours of work to complete all content.

This is divided as follows:

- 6x 3.5h on-site lab with examination
- 6x 5h preparation

Fig. 1: Semester allocation WS2025/26

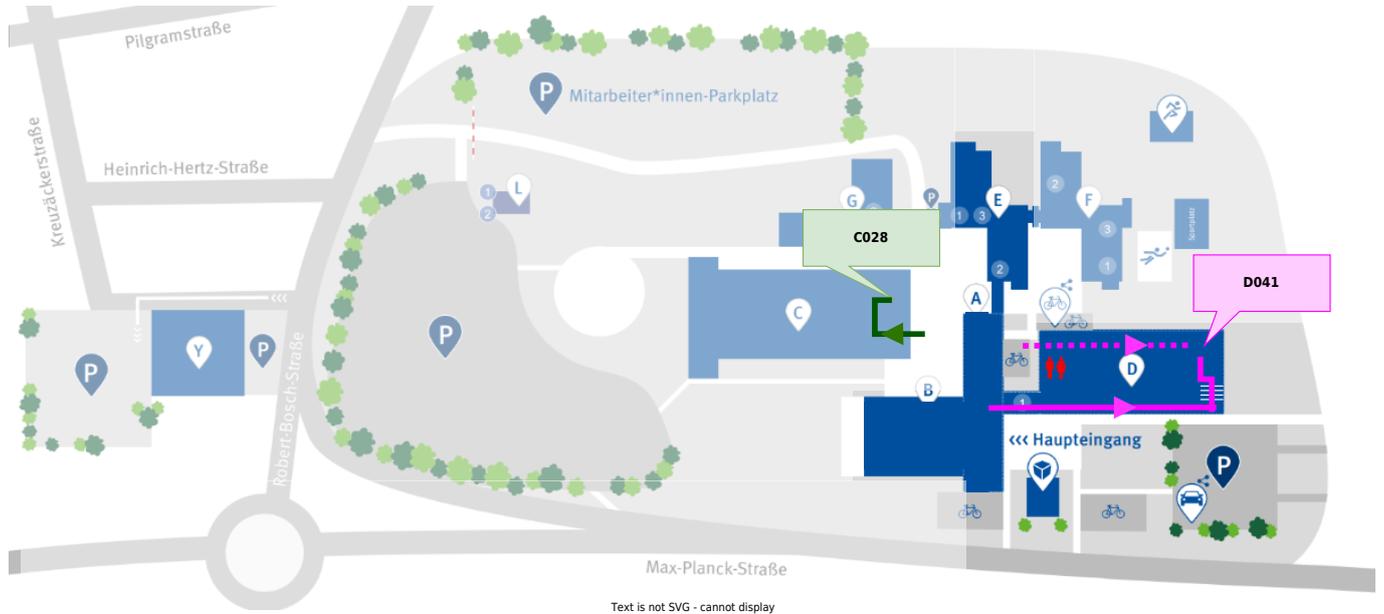
Location	Experiment	Date	Room	Group in the lab
On-site session	1 Resistors	18.03.2026	D041	GrA
		19.03.2026	D041	GrB
	2 Capacitors	26.03.2026	D041	GrB
		02.04.2026	D041	GrA
	3 Rectifier	09.04.2026	C028	GrA
		16.04.2026	C028	GrB
	4 AC Voltage	23.04.2026	D041	GrB (appointments for oral tests differ)
		30.04.2026	D041	GrA (appointments for oral tests differ)
	5 Operational Amplifier 1	07.05.2026	D041	GrA
		21.05.2026	D041	GrB
	6 Operational Amplifier 2	11.06.2026	C028	GrA + GrB
		18.06.2026	C028	GrA + GrB

## Oral examination

- Prepare well (see time allocation) for the oral examination; the difficulty level will increase over the semester.
- Being well prepared means you are able to explain concepts **without any aids** (also no papers of your own). You should be able to explain the topics using examples, sketches, mathematics, as well as current/voltage waveforms.
- For all experiments, you will find on the experiment's wiki page a list of bullet points that you should be able to explain freely.  
I also recommend delving deeper into the topics than just being able to explain the individual words.  
As literature sources, the [Additional Links](#) under EEE1 (German and English) can be used.
- Furthermore, you should have worked through the materials in ILIAS before the experiment.
- The oral examinations are planned for Friday during the experiment. The dates can be found in the group allocation in ILIAS.
- After each examination, I will give you brief feedback about my impression and the partial grade achieved.

## Location of the labs

The route to labs C028 and D041, where the experiments take place according to the allocation shown above, is sketched here:



## Further links

- The University of Deusto offers a remotely controlled real lab where you can get a first glimpse into various experiments: <https://labsland.com/en>

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Permanent link:  
[https://wiki.mexle.org/lab\\_electrical\\_engineering/start](https://wiki.mexle.org/lab_electrical_engineering/start)

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