

6 Inductances in Circuits

Student Group

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5. Inductances in Circuits

5.1 Basic Circuits

Focus here: uncoupled inductors!

5.1.1 Series Circuits

Based on $L = \frac{\Psi(t)}{i}$ and Kirchhoff's mesh law ($i = \text{const}$) the series circuit of inductions can be interpreted as a single current i which generates multiple linked fluxes Ψ . Since the current must stay constant in the series circuit, the following applies for the equivalent inductor of a series connection of single ones:

$$L_{\text{eq}} = \frac{\sum_i \Psi_i}{i} = \sum_i L_i$$

A similar result can be derived from the induced voltage $u_{\text{ind}} = L \frac{di}{dt}$, when taking the situation of a series circuit (i.e. $i_1 = i_2 = i_3 = \dots = i_{\text{eq}}$ and $u_{\text{eq}} = u_1 + u_2 + \dots$):

$$\begin{aligned} u_{\text{eq}} &= u_1 + u_2 + \dots \quad \& L_{\text{eq}} \frac{di_{\text{eq}}}{dt} &= L_1 \frac{di_1}{dt} + L_2 \frac{di_2}{dt} + \dots \\ & & &= L_1 \frac{di}{dt} + L_2 \frac{di}{dt} + \dots \end{aligned}$$

5.1.2 Parallel Circuits

For parallel circuits one can also start with the principles based on Kirchhoff's mesh law:

$$u_{\text{eq}} = u_1 = u_2 = \dots$$

and Kirchhoff's nodal law:

$$i_{\text{eq}} = i_1 + i_2 + \dots$$

Here, the formula for the induced voltage has to be rearranged:

$$\begin{aligned} u_{\text{ind}} &= L \frac{di}{dt} \quad \& \int u_{\text{ind}} dt &= L \int i dt \\ & & &= \frac{1}{L} \int u_{\text{ind}} dt \end{aligned}$$

By this, we get:

$$\begin{aligned} i_{\text{eq}} &= i_1 + i_2 + \dots \quad \& \frac{1}{L_{\text{eq}}} \int u_{\text{eq}} dt &= \frac{1}{L_1} \int u_1 dt + \frac{1}{L_2} \int u_2 dt + \dots \\ & & &= \frac{1}{L_1} \int u dt + \frac{1}{L_2} \int u dt + \dots \end{aligned}$$

Notice:

The inductor behaves in the parallel and series circuit similar to the resistor.

5.1.3 in AC Circuits

For AC circuits (i.e. with sinusoidal signals) the impedance Z based on the real part R and imaginary part X has to be considered. In order to do so, one has to solve:

$$\underline{Z} = \frac{\underline{u}}{\underline{i}}$$

With the induction $u_{\text{ind}} = -L \frac{di}{dt}$ we get:

$$\underline{Z} = \frac{L \frac{d\underline{i}}{dt}}{\underline{i}} = L \frac{d\underline{i}}{\underline{i} dt}$$

Without limiting the generality, one can assume the current i to be: $i = I \sqrt{2} e^{j\omega t + \varphi_0}$.

Therefore:

$$\begin{aligned} \underline{Z} &= L \frac{d \left(I \sqrt{2} e^{j\omega t + \varphi_0} \right)}{dt} \frac{1}{I \sqrt{2} e^{j\omega t + \varphi_0}} \\ &= L \frac{\left\{ \frac{d}{dt} \right\} \left(I \sqrt{2} e^{j\omega t + \varphi_0} \right)}{I \sqrt{2} e^{j\omega t + \varphi_0}} \\ &= L \frac{j \omega I \sqrt{2} e^{j\omega t + \varphi_0}}{I \sqrt{2} e^{j\omega t + \varphi_0}} \\ &= L j \omega \end{aligned}$$

Notice:

In AC calculations the inductor is described with a complex impedance $\underline{Z} = L j \omega$

5.2 Charging and Discharging

Charging and discharging an RL circuit is comparable to the RC-circuit in chapter [DC Circuit Transients](#) from last semester. Details are not covered here; see [OpenStax](#)

5.3 Resonance Phenomena

Similar to the approach last semester we now focus on circuits with inductors L . For preparation, please recap the chapter [Circuits under different Frequencies](#) from last semester.

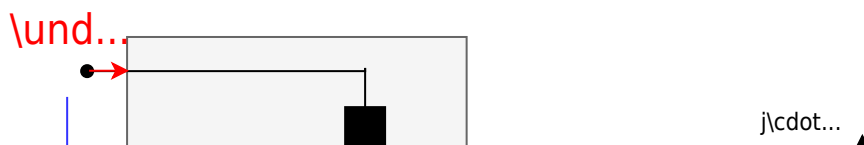
5.3.1 RLC - Series Resonant Circuit

As seen last semester, the circuits with complex impedances can be interpreted as four-terminal networks. There, we will again look at “output versus input”, i.e. $A_V = \frac{U_O}{U_I} \rightarrow \underline{A}_V = \frac{\underline{U}_O}{\underline{U}_I}$.

In this chapter we look at combination where all three components resistor R , capacitor C and

inductance L are used.

Fig. 1: circuit of the series resonant circuit



If a resistor R , a capacitor C and an inductance L are connected in series, the result is a **series resonant circuit**. In this case, it not clearly defined, what the output voltage is. Consequently, it must be considered how the voltages behave across all the individual components in the following.

The total voltage (= input voltage U_I) results to:

$$\underline{U}_I = \underline{U}_R + \underline{U}_L + \underline{U}_C$$

Since the current in the circuit must be constant, the total impedance can be determined here in a simple way:

$$\underline{U}_I = R \cdot \underline{I} + j\omega L \cdot \underline{I} + \frac{1}{j\omega C} \cdot \underline{I} \implies \underline{U}_I = \left(R + j\omega L - j \cdot \frac{1}{\omega C} \right) \cdot \underline{I} \implies \underline{Z} = R + j\omega L - j \cdot \frac{1}{\omega C}$$

By this, the magnitude of the (input) voltage U_I , the (input or total) impedance Z and the phase result to:

$$U_I = \sqrt{U_R^2 + (U_L - U_C)^2}$$

$$Z = \sqrt{R^2 + (\omega L - \frac{1}{\omega C})^2}$$

$$\varphi_u = \varphi_Z = \arctan \left(\frac{\omega L - \frac{1}{\omega C}}{R} \right)$$

There are now 3 different situations to distinguish:

- If $U_L > U_C$ the whole setup behaves like an ohmic-inductive load. This is the case at high frequencies.
- If $U_L = U_C$, the total input voltage U is applied to the resistor. In this case, the total resistance Z is minimal and only ohmic. Thus, the current I is then maximal. If the current is maximum, then the responses of the capacitance and inductance - their voltages - are also maximum. This situation is the **resonance case**.
- If $U_L < U_C$ then the whole setup behaves like a resistive-capacitive load. This is the case at low frequencies.

Again, there seems to be an excellent frequency, namely when $U_L = U_C$ or $Z_C = Z_L$ holds:

$$\frac{1}{\omega_0 C} = \omega_0 L \implies \omega_0 = \frac{1}{\sqrt{LC}} \implies f_0 = \frac{1}{2\pi \sqrt{LC}}$$

The frequency f_0 is called **resonance frequency**.

	$\mathbf{\rightarrow 0}$	$\mathbf{\rightarrow \infty}$
voltage U_R at the resistor	U	U
voltage U_L at the inductor	0	∞
voltage U_C at the capacitor	∞	0

The calculation in the table shows that in the resonance case, the voltage across the capacitor or

inductor deviates from the input voltage by a factor $\frac{1}{R} \sqrt{\frac{L}{C}}$. This quantity is called **quality or Q-factor** Q_S :

$$Q_S = \frac{U_C}{U} \Big|_{\omega = \omega_0} = \frac{U_L}{U} \Big|_{\omega = \omega_0} = \frac{1}{R} \sqrt{\frac{L}{C}}$$

The quality can be greater than, less than or equal to 1.

- If the quality is very high, the overshoot of the voltages at the impedances becomes very large in the resonance case. This is useful and necessary in various applications, e.g. in an RLC element as an antenna.
- If the Q is very small, overshoot is no longer seen. Depending on the impedance at which the output voltage is measured, a high-pass or low-pass is formed similar to the RC or RL element. However, this has a steeper slope in the blocking range. This means that the filter effect is better.

The reciprocal of the Q is called **attenuation** d_S . This is specified when using the circuit as a non-overshooting filter.

$$d_S = \frac{1}{Q_S} = R \sqrt{\frac{C}{L}}$$

Fig. 2: Amplitude and Phase Response of a Series Resonant Circuit

444 0. Amplitude Response

Series Resonant Circuit in Time Domain (Voltage on Inductor)

Series Resonant Circuit in Frequency Domain (Voltage on Inductor)

Series Resonant Circuit in Frequency Domain (Voltage on Capacitor)

Series Resonant Circuit in Frequency Domain (Voltage on Resistor)

Parallel Resonant Circuit in Frequency Domain (Voltage on Resistor)

5.4 Applications of Inductors

- ferrite bead
- Decoupling
- Filter
- unwanted coupling and circuit design

5.5 Examples

Decoupling Capacitor on the Microcontroller

[Simulation in Falstad](#). Note: The simulation gives a highly simplified picture. The response of the microcontroller is shown reduced to a triangular signal, since the slope of the voltages cannot be represented. A real simulation requires a powerful SPICE program in which the [conduction theory](#) can be represented.

Further details can be found [here \(practice\)](#), [here \(layout\)](#), also [Layout](#) or [Layout](#).

Crystal as Resonant Circuit

[more background](#)

Simulation in Time Domain

Simulation in Frequency Domain

[setup for the design](#)

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