

Falstad Simulations

Student Group

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Falstad Simulations

capacitic-ohmic Behaviour of a Wire

Photo-Diode

Timer Counter Simulation

Up/Down Counter

Battery Simulation

Fitting SOC-U curve <https://iopscience.iop.org/article/10.1088/1742-6596/1325/1/012173/pdf>

With the possibility to automatize:

four nodes with turn-off

Buck Converter

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