

Falstad Simulations

Student Group

First Name	Surname	Matrikel Nr.

Table of Contents

- Falstad Simulations** 2
- capacitic-ohmic Behaviour of a Wire* 2
- Photo-Diode* 2
- Timer Counter Simulation* 2
- Up/Down Counter* 2
- Battery Simulation* 2
- four nodes with turn-off* 2
- Buck Converter* 2

Falstad Simulations

capacitic-ohmic Behaviour of a Wire

Photo-Diode

Timer Counter Simulation

Up/Down Counter

Battery Simulation

Fitting SOC-U curve <https://iopscience.iop.org/article/10.1088/1742-6596/1325/1/012173/pdf>

With the possibility to automatize:

four nodes with turn-off

Buck Converter

From:

<https://wiki.mexle.org/> - MEXLE Wiki

Permanent link:

https://wiki.mexle.org/falstad_simulations?rev=1761749311

Last update: **2025/10/29 15:48**

