

# 4 Realization of Combinatorial Logic

## Student Group

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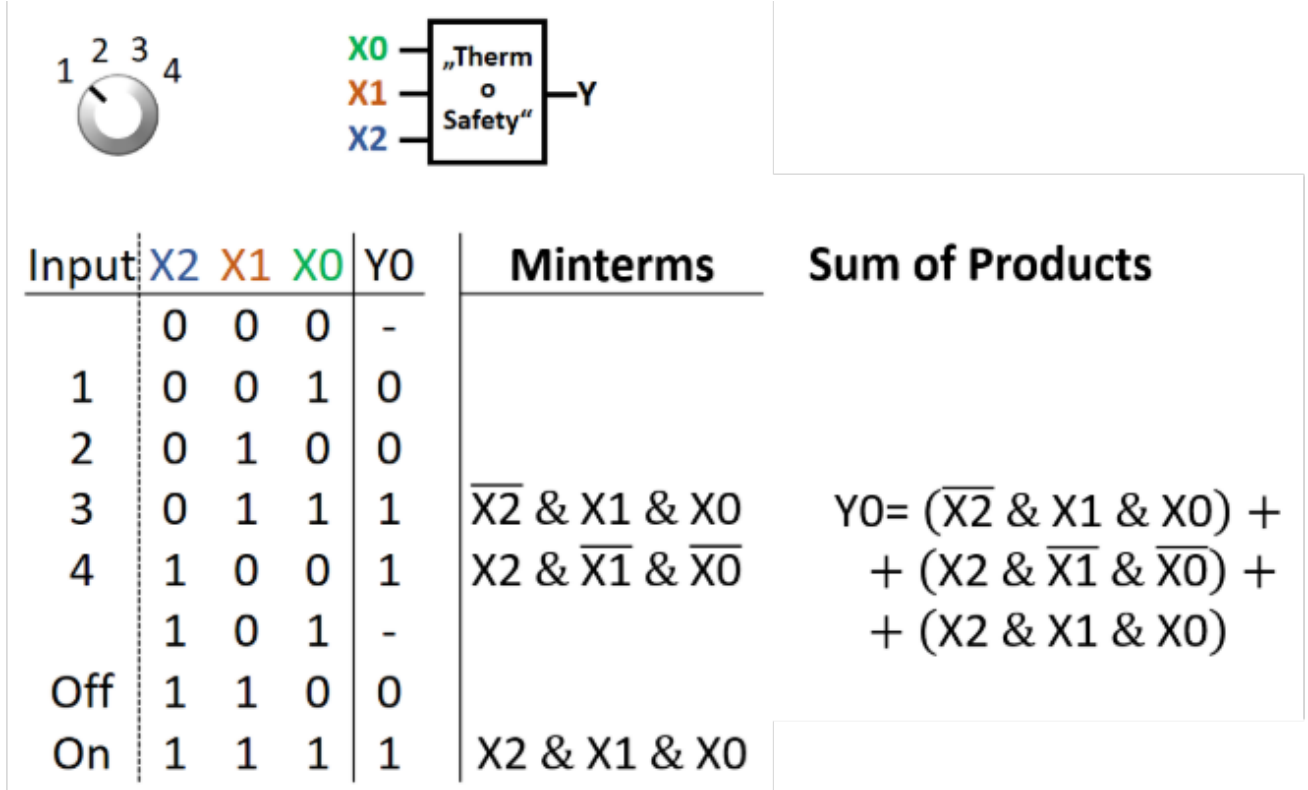
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# Realization of Combinatorial Logic

In the last chapter we investigated the Therm-o-Safety example. From this, it was possible to create the logic formula via sum of products of products of sums (see [figure 1](#)).

Fig. 1: gate logic from Therm-o-Safety example: Formula



It is simple to derive the gate logic from the formula ([figure 2](#)). For each of the three (min)terms an AND-gate combines the needed inputs. As shown in the image, usually parallel to the inputs also the inverted inputs are drawn.

Fig. 2: gate logic from Therm-o-Safety example: Gate Logic

# Gate Logic

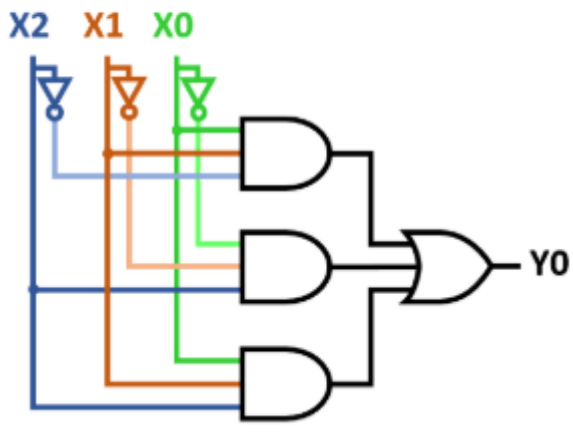


Fig. ##: Simulation of an Inverter

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